TITLE: NON-LINEAR DECISION FEEDBACK PHASE LOCKED LOOP FILTER INVENTORS NAME: Andrew Carter et al. SERIAL NO.: 10/090,435

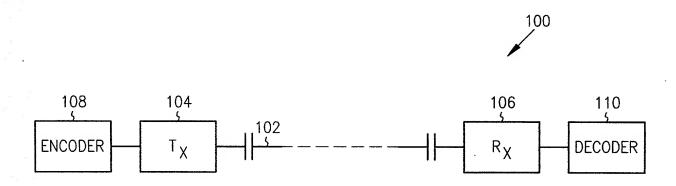


FIG. 1

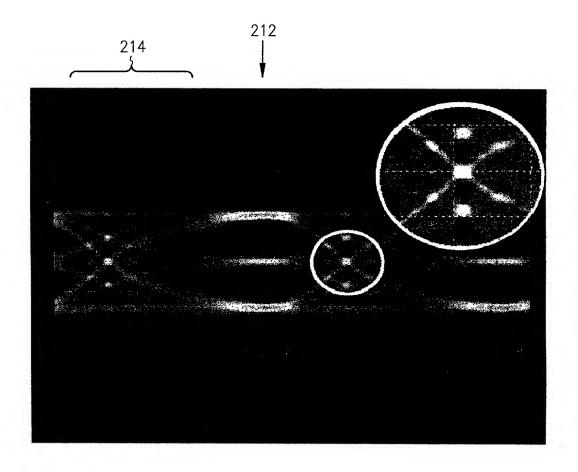
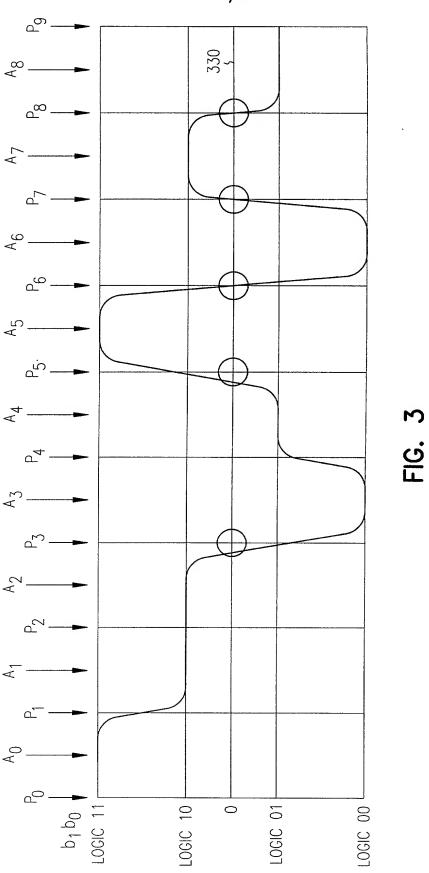


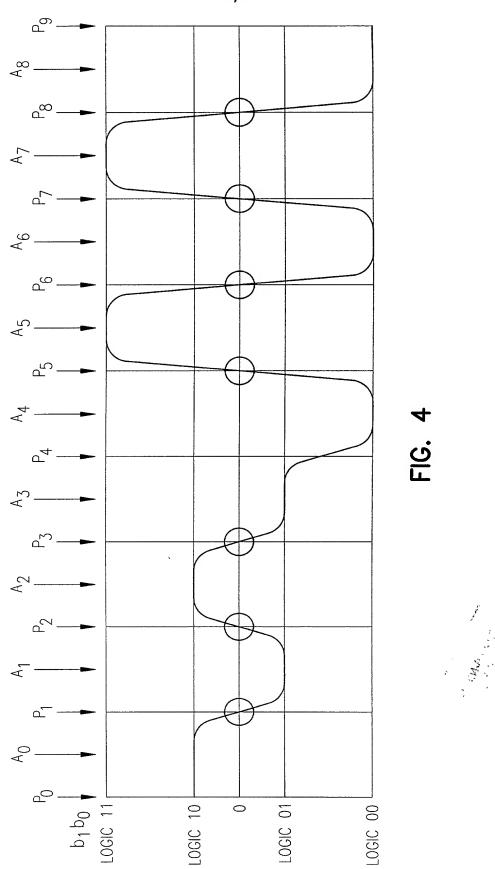
FIG. 2

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INVENTORS NAME: Andrew Costes of the INVENTORS NAME: Andrew Carter et al. SERIAL NO.: 10/090,435



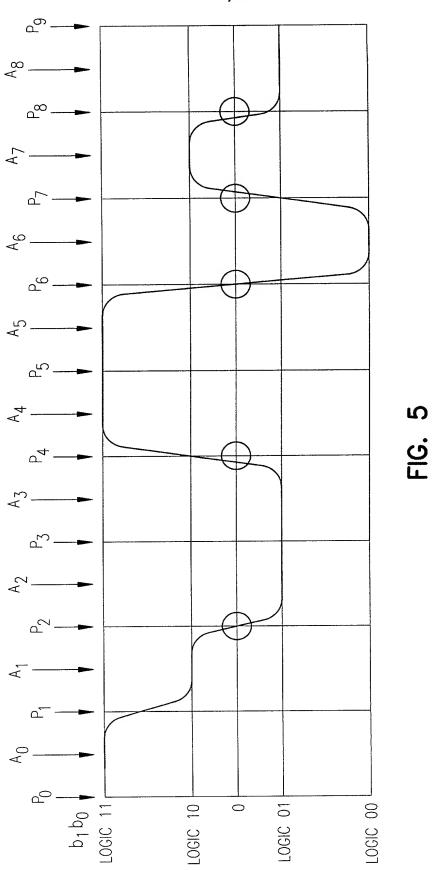


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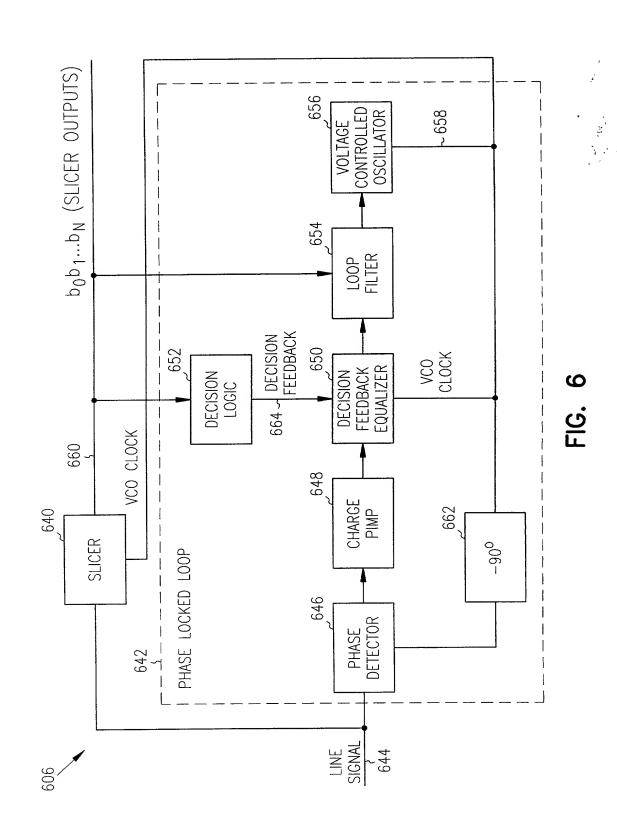
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NON-LINEAR DECISION FEEDBACK PHASE LOCKED LOOP FILTER INVENTORS NAME: Andrew Carter et al.

Andrew Carter et al. 10/090,435

SERIAL NO .:



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INVENTORS NAME: Andrew Carter et al. SERIAL NO.: 10/090,435

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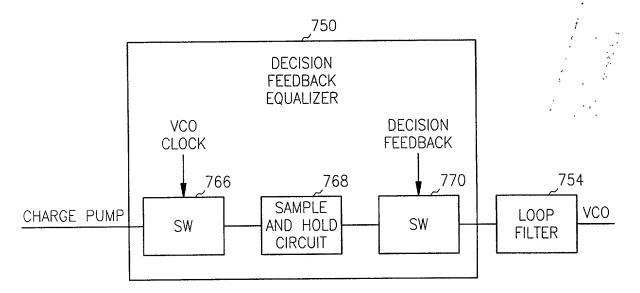


FIG. 7

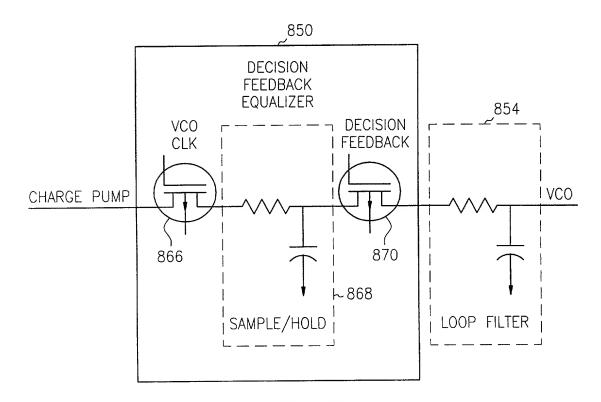
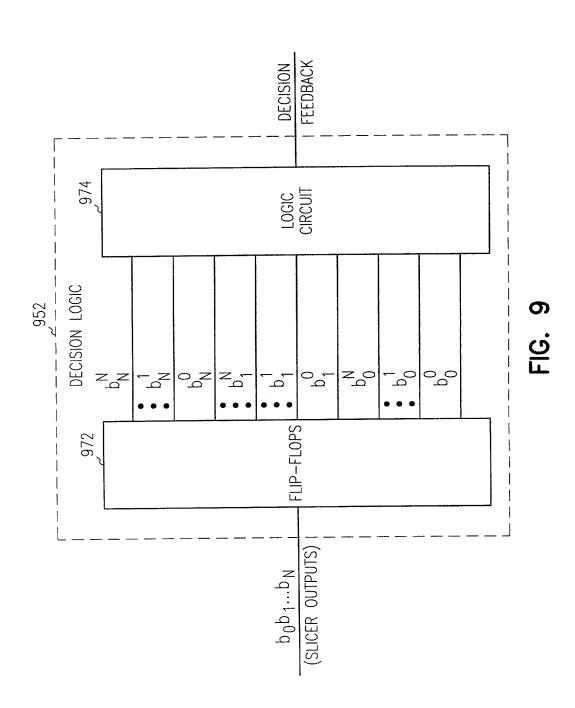


FIG. 8

10/090,435 SERIAL NO .:

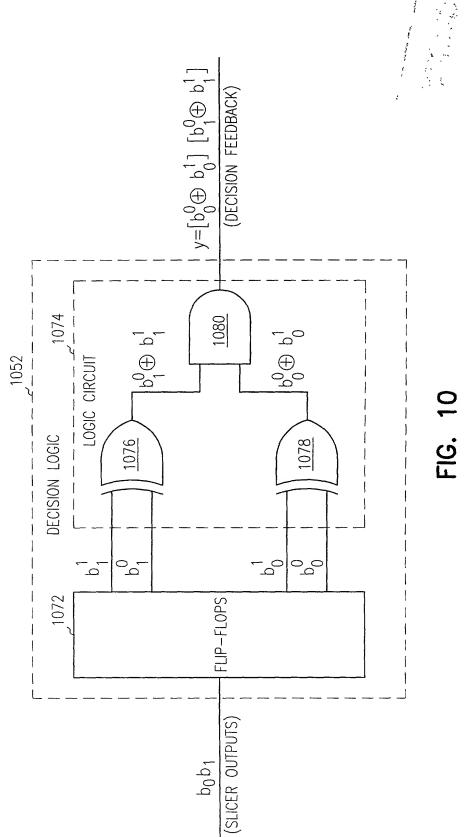


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INVENTORS NAME: Andrew Carter et al.

SERIAL NO.: 10/090,435





· TITLE: NON-LINEAR DECISION FEEDBACK PHASE LOCKED LOOP FILTER INVENTORS NAME: Andrew Carter et al. SERIAL NO.: 10/090,435 9/9 1110 RECEIVE SIGNAL FROM CHANNEL 1114 1112 **DETERMINE** DETECT PHASE OF AMPLITUDE LEVEL OF RECEIVED SIGNAL RECEIVED SIGNAL 1116 SAMPLE RECEIVED SIGNAL 1118 GOOD TRANSITION? NO B/W AMP. LEVELS YES 1120 PASS SAMPLED SIGNAL TO LOOP FILTER AND VCO TO ADJUST PHASE OF RECEIVER

FIG. 11

CLOCK AS APPROPRIATE